

Access DB# 275709**SEARCH REQUEST FORM****Scientific and Technical Information Center**

Requester's Full Name: Mano Padmanabhan Examiner #: 76556 Date: 10/20/08  
Art Unit: 2100 Phone Number 2-4210 Serial Number: 09/927210  
Mail Box Location: RND- 2A79 Results Format Preferred (circle): PAPER DISK E-MAIL

**If more than one search is submitted, please prioritize searches in order of need.**

\*\*\*\*\*

Please provide a detailed statement of the search topic, and describe as specifically as possible the subject matter to be searched. Include the elected species or structures, keywords, synonyms, acronyms, and registry numbers, and combine with the concept or utility of the invention. Define any terms that may have a special meaning. Give examples or relevant citations, authors, etc, if known. Please attach a copy of the cover sheet, pertinent claims, and abstract.

Title of Invention: \_\_\_\_\_

Inventors (please provide full names): \_\_\_\_\_

Earliest Priority Filing Date: \_\_\_\_\_

*\*For Sequence Searches Only\* Please include all pertinent information (parent, child, divisional, or issued patent numbers) along with the appropriate serial number.*

Litigation  
5,937,423

\*\*\*\*\*

**STAFF USE ONLY****Type of Search****Vendors and cost where applicable**

Searcher: <u>Shirelle Green</u>	Sequence (#) _____	STN _____
Searcher Phone #: <u>306-4767</u>	AA Sequence (#) _____	Dialog _____
Searcher Location: <u>4B40</u>	Structure (#) _____	Questel/Orbit <input checked="" type="checkbox"/>
Date Searcher Picked Up: <u>10/21</u>	Bibliographic <input checked="" type="checkbox"/>	Dr.Link _____
Date Completed: <u>10/21</u>	Litigation <input checked="" type="checkbox"/>	Lexis/Nexis <input checked="" type="checkbox"/>
Searcher Prep & Review Time: _____	Fulltext _____	Sequence Systems _____
Clerical Prep Time: <u>2</u>	Patent Family _____	WWW/Internet <input checked="" type="checkbox"/>
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1 of 1 DOCUMENT

UNITED STATES PATENT AND TRADEMARK OFFICE GRANTED PATENT

**5937423**

[Link to Claims Section](#)

August 10, 1999

Register interface for flash EEPROM memory arrays

**REISSUE:** August 9, 2001 - Reissue Application filed Ex. Gp.: 2752; Re. S.N. 09/927,210 (O.G. October 30, 2001)

**INVENTOR:** Robinson, Kurt B. - Newcastle, California, United States (US)

**APPL-NO:** 773168 (08)

**FILED-DATE:** December 26, 1996

**GRANTED-DATE:** August 10, 1999

**ASSIGNEE-PRE-ISSUE:** December 26, 1996 - ASSIGNMENT OF ASSIGNORS INTEREST (SEE DOCUMENT FOR DETAILS)., INTEL CORPORATION 2200 MISSION COLLEGE BOULEVARD SANTA CLARA, CALIFORNIA, 95052, Reel and Frame Number: 008358/0696

**ASSIGNEE-AT-ISSUE:** Intel Corporation, Santa Clara, California, United States (US), United States company or corporation (02)

**ENGLISH-ABST:**

A flash EEPROM memory device including a memory array having a plurality of blocks of flash EEPROM memory cells arranged to be accessed in rows and columns, a query memory storing data defining characteristics of the flash storage device, and a register interface for receiving data and commands addressed to the blocks of flash EEPROM memory devices and generating signals for affecting the purpose of the commands in the device, the interface including a command register for receiving commands and a plurality of registers for providing the data stored in the query memory as output.

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Query/Command : us5937423/pn

\*\* SS 3: Results 1

Search statement 4

Query/Command : prt full nonstop legalall

1 / 1 PLUSPAT - Questel - image  
PN - US5937423 A 19990810 [US5937423]  
TI - (A) Register interface for flash EEPROM memory arrays  
PA - (A) INTEL CORP (US)  
PAO - Intel Corporation, Santa Clara CA [US]  
IN - (A) ROBINSON KURT B (US)  
AP - US77316896 19961226 [1996US-0773168]  
PR - US77316896 19961226 [1996US-0773168]  
IC - (A) G06F-012/02  
ICAA- G06F-003/06 [2006-01 A - I R M EP]; G11C-016/10 [2006-01 A - I R M EP]  
ICCA- G06F-003/06 [2006 C - I R M EP]; G11C-016/06 [2006 C - I R M EP]  
EC - G06F-003/06E  
G11C-016/10  
PCL - ORIGINAL (O) : 711103000; CROSS-REFERENCE (X) : 365052000  
DT - Corresponding document  
CT - US5053990 [US5053990] 564900000  
-US5307320 [US5307320] 365230010  
-US5341330 [US5341330] 365185000  
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-US5684997 [US5684997] 395733000  
-US5696929 [US5696929] 395430000  
-US5727057 [US5727057] 379211000  
-US5740395 [US5740395] 395430000  
-US5754567 [US5754567] 371040180  
-US5761732 [US5761732] 711157000  
STG - (A) United States patent  
AB - A flash EEPROM memory device including a memory array having a plurality of blocks of flash EEPROM memory cells arranged to be accessed in rows and columns, a query memory storing data defining characteristics of the flash storage device, and a register interface for receiving data and commands addressed to the blocks

of flash EEPROM memory devices and generating signals for affecting the purpose of the commands in the device, the interface including a command register for receiving commands and a plurality of registers for providing the data stored in the query memory as output.

1 / 1 LGST - ©EPO  
PN - US5937423 A 19990810 [US5937423] (A) Patent  
AP - US77316896 19961226 [1996US-0773168]  
PUB - 19961226 US-API [POS; EXM]  
FILING DETAILS  
US77316896 19961226 [1996US-0773168]  
19990810 US-A [POS; EXM]  
Patent  
US5937423 A 19990810 [US5937423]  
ACT - 20011030 US/RF-A [OPP]  
REISSUE APPLICATION FILED  
EFFECTIVE DATE: 20010809  
LEG - OPP  
Alive  
UP - 2003-22

1 / 1 CRXX - ©CLAIMS/RRX  
PN - 5,937,423 A 19990810 [US5937423]  
PA - Intel Corp  
ACT - 20010809 REISSUE REQUESTED  
ISSUE DATE OF O.G.: 20011030  
REISSUE REQUEST NUMBER: 09/927210  
EXAMINATION GROUP RESPONSIBLE FOR REISSUEPROCESS: 2752

Reissue Patent Number: